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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	10001668	FILING DATE	10/23/2001	CLASS	361	SUBCLASS		GAU	2844	EXAMINER	
**APPLICANTS: Gehrke Martin; Pechstein Torsten;											
**CONTINUING DATA VERIFIED:											
** FOREIGN APPLICATIONS VERIFIED: GERMANY 100 52 532.6 10/23/2000											
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>											
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials										ATTORNEY DOCKET NO DFS-138-A	
TITLE : Design for constructing an input circuit to receive and process an electrical signal											

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Assistant Examiner	
Amount Due	Date Paid	Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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